

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Masahiro KOYAMA et al.

Serial No.: Rule 53(b) Continuation of U.S. Application No. 09/398,776

Filed: March 31, 2004

For: DECENTRALIZED CONTROL SYSTEM FOR NETWORK
CONNECTION

Group: Not yet assigned

Examiner: Not yet assigned

**INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & 1.98**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

March 31, 2004

Sir:

In the matter of the above-identified application, Applicants are submitting herewith the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

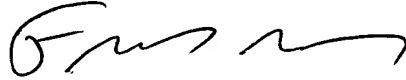
This information disclosure statement is being submitted concurrently with the application.

Each of the documents listed on the attached form equivalent to Form PTO-1449 was cited or submitted in the parent application, U.S. Application No. 09/398,776, filed March 10, 1999.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus Deposit Account No. 01-2135 (referencing attorney docket no. 500.37600CX1) please credit any excess fees to such deposit account.

Respectfully submitted,



Frederick D. Bailey
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ANTONELLI, TERRY, STOUT & KRAUS, LLP

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FORM PTO-1449 U.S. Department of
Commerce (Rev. 4/92) Patent and Trademark
Office

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

ATTY. DOCKET NO.

500.37600CX1

SERIAL NO.

**Rule 53(b) of U.S.
Application No.
09/398,776**

APPLICANT

Masahiro KOYAMA et al.

FILING DATE

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GROUP

Not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,555,179	09-1996	KOYAMA et al.			
	5,592,671	01-1997	HIRAYAMA			
	6,370,560	04-2002	ROBERTAZZI et al.			
	6,128,279	10-2000	O'NEIL et al.			
	6,223,205	04-2001	HARCHOL-BLATER et al.			
	6,449,711	09-2002	WEEK			
	5,142,683	08-1992	BURKHARDT et al.			
	4,769,771	09-1998	LIPMANN et al.			
	5,418,953	05-1995	HUNT et al.			

FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
					YES	NO
JP 10-177495	6-1998	Japan				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Sterling et al., "Communication Overhead for Space Science Application on the Beowulf Parallel Workstation", IEEE, p. 23-30, 1995.
	Sterling et al., A Design Study of Alternative Network Topologies for the Beowulf Parallel Workstation", IEEE, p. 626-635, 1996.
	Ridge et al., "Beowulf: Harnessing the Power of Parallelism in a Pile-of-PCs", IEEE, p. 79-91, 1997.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])

SHEET 2 OF 2

FORM PTO-1449 U.S. Department of Commerce (Rev. 4/92) Patent and Trademark Office INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	ATTY. DOCKET NO. 500.37600CX1	SERIAL NO. Rule 53(b) of U.S. Application No. 09/398,776
	APPLICANT Masahiro KOYAMA et al.	
	FILING DATE March 31, 2004	GROUP Not yet assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,961,585	10-1999	HAMLIN			
	5,887,143	03-1999	SAITO et al.			
	5,187,791	02-1993	BAUM			
	5,138,708	08-1992	VOSBURY			
	6,018,759	01-2000	DOING et al.			
	5,452,452	09-1995	GAETNER et al.			
	5,745,452	04-1998	LECOURTIER			
	6,567,839	05-2003	BORKENHAGEN et al.			
	6,363,453	03-2002	ESPOSITO et al.			
	5,844,888	12-1998	MARKKULA et al.			
	6,219,073	04-2001	SUZUOKI			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Lennart LINDH, "FASTHARD - A Fast Time Deterministic HARDware Based Real-Time Kernel", IEEE, p. 21,-25, 1992.
	Starner et al., "Real-Time Scheduling Co-Processor in Hardware for Single and Multiprocessor Systems", IEEE, p. 509-512, 1996.

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EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5,838,912	11-1998	POON et al.			
	5,185,861	02-1993	VALENCIA			

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	ABSTRACT	
						YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Frank Stanischewski, "FASTCHART - Performance, Benefits and Disadvantages of the Architecture", IEEE, p. 246-250, 1993.
	Adomat et al. "Real-Time Kernel in Hardware RTU: A Step Towards Deterministic and High-Performance Real-Time Systems", p. 164-168, 1996.

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DATE CONSIDERED

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